Educational

Title	Date (since–to)	Institute	Activities
High School Graduate	1994-1999	High School of Mantova, Technical Industrial Institute Enrico Fermi	Graduation marks: 70/100
M.Sc. in Electronic Engineering	1999-2006	University of Parma (Italy) Faculty of Engineering Course: Electronic Engineering	Graduation marks: 92/110 Thesis title: "Design of operational amplifiers for pipelined A/D converters in CMOS and BiCMOS tecnologies ".

Foreign Language

Language	Knowledge
English	Good knowledge of spoken and written English

Work Experience

Company	Date (since–to)	Job title	Main Activities
University of Parma (Italy) Department of Information Engineering	2007	Intern	Design, layout and lab testing of: Integrated Lock-in amplifier in 0.35um CMOS
Silis s.r.l., Parma (Italy) – design and consulting company	2008 - today	Senior Analog/Mixed-Signal IC's designer	Design, layout and lab testing of: Bandgap for RFID BAP-chip. Passive Mixer and Switched Capacitors Offset-cancellation stages for Lock-In amplifier. Low Noise Amplifier (LNA) for low frequency application. A 12-bit 1 MHz Sigma-Delta A/D converter . Ultra Low power and Low Noise LDO for integrated temperature sensor for RFID device. Low Power Relaxation Oscillator for Sensor-RFID.

			32.768 kHz ultra low power Crystal Oscillator with internal AGC. Tunable Gm-C Filter for audio application. Ultra low noise Amplifier for audio application. Low power/voltage Sigma-Delta ADC for Sensor-RFID. Lock-In amplifier for contactless interface to magnetically stimulated mechanical resonators in CMOS 350nm technology. Rail to rail opamp for automotive application.
	2009	Analog Consultant at PDF Solutions (Desenzano, Italy)	Low-frequency characterization of packaged ASIC. On-wafer device characterization and measurements. Probe-card design Use of 12' semi-automatic probestation.
	2010	Analog Consultant at ST Microelectronics (Milan, Italy)	Design and layout of: A 18-bit 1 MHz Sigma-Delta A/D converter . A 12-bit 16 MHz Multibit Sigma-Delta A/D converter .
	Dec 2011 - Apr 2012	Analog Consultant at Maxim Integrated (Milan, Italy)	Design and layout of: Low power LDO. Low power stand-by Regulator. Rail to Rail opamp.
	Aug 2012 - Nov 2012	Analog Consultant at Maxim Integrated (Milan, Italy)	Design and layout of: Power on reset circuit. Low power LDO for analog section. Low power LDO for digital section.
	Feb 2013 – Sep 2013	Analog Consultant at Maxim Integrated (Milan, Italy)	Design and layout of: Bandgap. Low power LDO. Temperature Sensor. Low Power Relaxation Oscillator.
	Sep 2014 – jul 2015	Analog Consultant at ams AG (Unterpremstaetten, Austria)	Design of: Rail to Rail Opamp. AutoZero Ping-Pong Amplifier. Current Amplifier. High speed Input ADC buffer. High accurancy Relaxation

			Oscillator.
	2016	Analog Consultant at ams AG (Unterpremstaetten, Austria)	Design of: Bandgap. Low power LDO. Low noise Operational Amplifiers.
	2017	Senior Analog Designer at Silis s.r.l. Parma (Pr), Italy	Design and layout of: Operational Amplifier for 12b, 128MS/s pipeline ADC
	2018 - Now	Analog Consultant at ST Microelectronics (Milan, Italy)	Design of: Power Management Circuit Instrumentation Amplifier with common mode control Sample and Hold

IT/CAD knowledge

Category	Applications
Design and Simulation Systems	Systems CadenceTM Virtuoso Composer and ADE flow up to 6.0, ADE, SpectreTM, SpectreRFTM, OCEANTM; Mentor GraphicsTM Design Architect IC, EldoTM.
Analog IC layout	CadenceTM Virtuoso Layout/Layout XL flow, Assura, Mentor GraphicsTM Calibre.
Mixed Signal IC	CadenceTM ICMS/ICFB, AMS-Designer plug-in.
Digital IC design	basic VHDL language.
PCB design	CadenceTM Pspice,Orcad Capture and Layout.

Job skills

Category	Applications
Analog/Mixed signal IC design	Very good knowledge of design methodology for low power/voltage analog cells.
Analog/Mixed signal IC layout	Very good analog layout experience with parasitic and area optimization.
Lab testing	Good knowledge of standard lab instrumentation and experience in samples testing and characterization.
Relational skills and flexibility	Availability to travel abroad (short periods), good relational skills, able to work in dynamic environment with flexible timetable.

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